

# Multiprocessor Systems On Chip: Design Space Exploration

## By Torsten Kempf

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### **dblp: Stefan Wallentowitz -**

in Manycore Processor Systems. Multiprocessor System-on-Chip 2011 Torsten Kempf, Stefan Wallentowitz, Simulation Based Design Space Exploration of

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methodology for architectural exploration of Torsten Kempf Institute for rapid design space exploration at the System Level.

**Ghiath Al-kadi -**

Fields of study: Hardware & Architecture, Multimedia, Real-Time based Design Space Exploration of of a Multiprocessor System-on-Chip Architecture

**Rainer Leupers - AbeBooks -**

Multiprocessor Systems on Chip: Design Space Exploration. Torsten Kempf, Gerd Ascheid, Rainer Leupers. Concluding Remarks. 5: Architecture Exploration. 1.

**eBooks by Rainer Leupers -**

emphasize that neither the architectural advantages nor the design space of ASIPs are sacrificed for an automated implementation. Processor and System-on-Chip

**Multiprocessor System- on-Chip - Springer -**

Multiprocessor System-on-Chip Hardware Design and Tool Integration. Physical Design of Multiprocessor Systems. Front Matter. Pages 153-153. Download PDF

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Multiprocessor Systems on Chip. Torsten Kempf, Gerd Ascheid, Rainer Leupers. focusing on early design space exploration.

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The Institute for Communication Technologies and Embedded Systems Torsten Kempf; Assistants; in Proceedings of the International Symposium on System-on-Chip

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Multiprocessor systems on chip. New York : Springer, 2011 (DLC) 2011921340

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